RAY I and II Series TTL (Cont.)

Type[1] Number	Description	Fanout Function	TYPICAL CHARACTERISTICS: Tpd (ns) or Toggle Rate (Min)	TYPICAL CHARACTERISTICS: Avg. Pwr. Function (mW) 50% Duty	TYPICAL CHARACTERISTICS: DC Noise Margin (V)	Available Packages: 14 Pin CJ	14 Pin CK	14 Pin D	14 Pin DC
RF9601	Retriggerable monostable multivibrator (-55°C to +125°C)	10 mA	25	100	+1.0, -1.5	X	X	X	X
RF9602	Retriggerable monostable multivibrator (0°C to +75°C)	12.8 mA	25	100	+1.0, -1.5				
RG40	Dual 4 input NAND gate	15	10	15/gate	+1.1, -1.5	X	X	X	X
RG41	Dual 4 input NAND gate	7	10	15/gate	+1.1, -1.5	X	X	X	X
RG42	Dual 4 input NAND gate	12	10	15/gate	+1.1, -1.5	X	X	X	X
RG43	Dual 4 input NAND gate	6	10	15/gate	+1.1, -1.5	X	X	X	X
RG50	Exp. 4-wide, 2-2-2-3 input AOI gate	15	12	30	+1.1, -1.5	X	X	X	X
RG51	Exp. 4-wide, 2-2-2-3 input AOI gate	7	12	30	+1.1, -1.5	X	X	X	X
RG52	Exp. 4-wide, 2-2-2-3 input AOI gate	12	12	30	+1.1, -1.5	X	X	X	X
RG53	Exp. 4-wide, 2-2-2-3 input AOI gate	6	12	30	+1.1, -1.5	X	X	X	X
RG60	Single 8 input NAND gate	15	12	15	+1.1, -1.5	X	X	X	X
RG61	Single 8 input NAND gate	7	12	15	+1.1, -1.5	X	X	X	X
RG62	Single 8 input NAND gate	12	12	15	+1.1, -1.5	X	X	X	X
RG63	Single 8 input NAND gate	6	12	15	+1.1, -1.5	X	X	X	X
RG70	Dual 2-wide, 2 input AOI gate, one side exp.	15	12	20/gate	+1.1, -1.5	X	X	X	X
RG71	Dual 2-wide, 2 input AOI gate, one side exp.	7	12	20/gate	+1.1, -1.5	X	X	X	X
RG72	Dual 2-wide, 2 input AOI gate, one side exp.	12	12	20/gate	+1.1, -1.5	X	X	X	X
RG73	Dual 2-wide, 2 input AOI gate, one side exp.	6	12	20/gate	+1.1, -1.5	X	X	X	X
RG80	Dual pulse shaper/delay AND gate	15	11	30/gate	+1.1, -1.5	X	X	X	X
RG81	Dual pulse shaper/delay AND gate	7	11	30/gate	+1.1, -1.5	X	X	X	X
RG82	Dual pulse shaper/delay AND gate	12	11	30/gate	+1.1, -1.5	X	X	X	X
RG83	Dual pulse shaper/delay AND gate	6	11	30/gate	+1.1, -1.5	X	X	X	X
RG90	Exclusive OR gate with complement	15	11	35	+1.1, -1.5	X	X	X	X
RG91	Exclusive OR gate with complement	7	11	35	+1.1, -1.5	X	X	X	X
RG92	Exclusive OR gate with complement	12	11	35	+1.1, -1.5	X	X	X	X
RG93	Exclusive OR gate with complement	6	11	35	+1.1, -1.5	X	X	X	X
RG100	Exp. 3-wide, 3 input AOI gate	15	12	25	+1.1, -1.5	X	X	X	X
RG101	Exp. 3-wide, 3 input AOI gate	7	12	25	+1.1, -1.5	X	X	X	X
RG102	Exp. 3-wide, 3 input AOI gate	12	12	25	+1.1, -1.5	X	X	X	X
RG103	Exp. 3-wide, 3 input AOI gate	6	12	25	+1.1, -1.5	X	X	X	X
RG110	Exp. 2-wide, 4 input AOI gate	15	12	20	+1.1, -1.5	X	X	X	X
RG111	Exp. 2-wide, 4 input AOI gate	7	12	20	+1.1, -1.5	X	X	X	X
RG112	Exp. 2-wide, 4 input AOI gate	12	12	20	+1.1, -1.5	X	X	X	X
RG113	Exp. 2-wide, 4 input AOI gate	6	12	20	+1.1, -1.5	X	X	X	X
RG120	Expandable single 8 NAND gate	15	18	15/gate	+1.1, -1.5	X	X	X	X
RG121	Expandable single 8 NAND gate	7	18	15/gate	+1.1, -1.5	X	X	X	X
RG122	Expandable single 8 NAND gate	12	18	15/gate	+1.1, -1.5	X	X	X	X
RG123	Expandable single 8 NAND gate	6	18	15/gate	+1.1, -1.5	X	X	X	X
RG130	Dual 4-input line driver	30	15	30/gate	+1.1, -1.5	X	X	X	X
RG131	Dual 4 input line driver	30	15	30/gate	+1.1, -1.5	X	X	X	X
RG132	Dual 4 input line driver	24	15	30/gate	+1.1, -1.5	X	X	X	X
RG133	Dual 4 input line driver	12	15	30/gate	+1.1, -1.5	X	X	X	X

1. Operating temperature range, final digits 0 or 1: -55°C to +125°C; final digits 2 or 3: 0°C to +70°C.